

Notice of References Cited	Application/Control No. 10/033,156	Applicant(s)/Patent Under Reexamination BEASOM, JAMES D.	
	Examiner Thomas J. Magee	Art Unit 2811	Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-			
	B	US-			
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FOREIGN PATENT DOCUMENTS

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NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	S. Wolff and R.N. Tauber, "Silicon Processing for the VLSI Era: Volume 1 - Process Technology," Lattice Press, Sunset Beach, CA (1986), pp. 321-324.
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*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
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